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PATENT
Attorney Docket No. ASC-044
(366/29)

#16

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Fitzgerald et al. CONFIRMATION NO.: 2812
SERIAL NO.: 09/884,172 GROUP NO.: 2822
FILING DATE: June 19, 2001 EXAMINER: Lattin, C.
TITLE: Method of Fabricating CMOS Inverter and Integrated Circuits
Utilizing Strained Silicon Surface Channel MOSFETs

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

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AUG 29 2003
TECHNOLOGY CENTER 2800

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the provisions of 37 C.F.R. 1.97 and 1.98, Applicants hereby make of record the patents and publications listed on the accompanying Form PTO-1449, and other information contained herein, for consideration by the Examiner in connection with the examination of the above-identified patent application. Copies of the patents and publications are enclosed.

REMARKS

In accordance with the provisions of 37 C.F.R. 1.97, this statement is being filed (CHECK ONE):

- ☒ (1) within three (3) months of the **filing date** of a national application other than a continued prosecution application under 37 C.F.R. 1.53(d), or within three (3) months of the **date of entry of the national stage** as set forth in 37 C.F.R. 1.491 in an international application, or before the mailing of the **first Office action** on the merits, or before the mailing of a **first Office action** after the filing of a request for continued examination under 37 C.F.R. 1.114; or
- ☐ (2) after the period defined in (1) but before the mailing date of a **final action** or a **notice of allowance** under 37 C.F.R. 1.311, and
- ☐ the requisite Statement is below, **OR**
- ☐ the requisite fee under 37 C.F.R. 1.17(p), namely **\$180.00**, is included herein, or
- ☐ (3) after the mailing date of a **final action** or **notice of allowance** but before the payment of the **issue fee**, **AND**
- ☐ the requisite Statement is below, **AND**
- ☐ the requisite petition fee under 37 C.F.R. 1.17(p), namely **\$180.00** is included herein.

In addition, Applicants wish to inform the Examiner about the following co-pending patent Applications:

- U.S. Serial No. 09/599,260, filed on June 22, 2000, by Wu et al.
- U.S. Serial No. 09/764,177, filed on January 7, 2001, by Fitzgerald.
- U.S. Serial No. 09/764,182, filed on January 17, 2001, by Fitzgerald.
- U.S. Serial No. 09/906,545, filed on July 16, 2001, by Fitzgerald.
- U.S. Serial No. 10/264,935, filed on October 4, 2002, by Lochtefield et al.

It is respectfully requested that each of the patents and publications listed on the attached Form PTO-1449, and other information contained herein, be made of record in this application.

STATEMENT

As required under 37 C.F.R. 1.97(e), Applicant(s), through the undersigned, hereby state either that [check the appropriate space only if either (2) or (3) is checked on the previous page and the Statement is required]:


- ☐ 1. Each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application **not more than three months** prior to the filing of the Information Disclosure Statement; or
- ☐ 2. No item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this Statement after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to **any individual** designated in 37 C.F.R. 1.56(c) **more than three months** prior to the filing of the Information Disclosure Statement.

Respectfully submitted,

Date: May 21, 2003
Reg. No. 44,381

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The "RECEIVED" stamp of the Patent Office imprinted hereon acknowledges the filing of: Transmittal Form (1 page); Supplemental Information Disclosure Statement (2 pages); Form PTO-1449 (7 pages); Cited References A46-A130, B25-B43, and C48-C86; Certificate of First Class Mailing dated May 21, 2003; (1 page); and this return receipt postcard.

Name of Applicant:

Fitzgerald et al.

Intf. or Serial Number:

09/884,172

Atty.:

Natasha C. Us/ARF/meh

Date:

May 21, 2003

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August 28, 2003

Examiner Christopher W. Lattin
United States Patent and Trademark Office
Crystal Plaza 4, 3B28
Arlington, VA 22202

Re: Pending United States Patent Application Serial No. 09/884,172
"Method of Fabricating CMOS Inverter and Integrated Circuits Utilizing
Strained Silicon Surface Channel MOSFETs" by Fitzgerald et al.
Our Docket No. ASC-044 (366/72)

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Dear Examiner Lattin:

In accordance with our conversation of August 27, 2003, enclosed for your consideration are replacement copies of the Supplemental Information Disclosure Statement, Form 1449, and the cited references labeled B25 to B43 and C48 to C86 that we filed May 21, 2003, as evidenced by the stamped postcard.

Please acknowledge receipt of these documents by returning the enclosed postcard. Please contact us if you have any questions.

Very truly yours,

Natasha C. Us

Natasha C. Us

Enclosures

cc: Steven J. Frank, Esq. (w/o encls.)

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